

| | | | |
|---|-------------------------------|------------------------------|--|
| Examiner-Initiated Interview Summary | Application No. 10/037,541 | Applicant(s) BROWN ET AL. | |
| | Examiner John P. Trimmings | Art Unit 2138 | |

All Participants:
Status of Application: Pending

 (1) John P. Trimmings.

(3) _____.

 (2) David Hoffman.

(4) _____.

Date of Interview: 5 January 2006
Time: 4:00 pm
Type of Interview:

- ☒ Telephonic
☐ Video Conference
☐ Personal (Copy given to: ☐ Applicant ☐ Applicant's representative)

Exhibit Shown or Demonstrated: ☐ Yes ☒ No

If Yes, provide a brief description:

Part I.
Rejection(s) discussed:

35 USC 103

Claims discussed:

1,9,162124

Prior art documents discussed:

To Li and Tavallaei

Part II.
SUBSTANCE OF INTERVIEW DESCRIBING THE GENERAL NATURE OF WHAT WAS DISCUSSED:

See Continuation Sheet

Part III.

- ☒ It is not necessary for applicant to provide a separate record of the substance of the interview, since the interview directly resulted in the allowance of the application. The examiner will provide a written summary of the substance of the interview in the Notice of Allowability.
☐ It is not necessary for applicant to provide a separate record of the substance of the interview, since the interview did not result in resolution of all issues. A brief summary by the examiner appears in Part II above.


 (Examiner/SPE Signature)

 (Applicant/Applicant's Representative Signature – if appropriate)

Continuation of Substance of Interview including description of the general nature of what was discussed:
The applicant and the examiner have agreed to the following claim amendments:

As per Claim 1:

Lines 6 and 7 are amended as follows:

at least one integrated circuit disposed in the managed server and connected for operative communication to the JTAG interface, wherein the at least one integrated circuit comprises a microprocessor, and ...

As per Claim 4:

Claim 4 is cancelled without traverse.

As per Claim 9:

Lines 7 and 8 are amended as follows:

JTAG master having a JTAG interface, the at least one integrated circuit connected for operative communication to the JTAG interface, wherein the at least one integrated circuit comprises a microprocessor, and, ...

As per Claim 11:

Claim 11 is cancelled without traverse.

As per Claim 16:

Line 10 is amended as follows:

JTAG interface[.], wherein the integrated circuit comprises a microprocessor.

As per Claim 21:

Line 6 is amended as follows:

Management controller[.], wherein the integrated circuit comprises a microprocessor.

As per Claim 23:

Claim 23 is cancelled without traverse.

As per Claim 24:

Lines 7 and 8 are amended as follows:

an integrated circuit disposed in the managed server and connected for operative communication to the JTAG interface, wherein the integrated circuit comprises a microprocessor, and



John P Trimmings
Examiner